Improved Multi-processor Scheduling for Flow Time and Energy

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Abstract

Energy usage has been an important concern in recent research on online scheduling. In this paper we study the tradeoff between flow time and energy [1,5,7,19] in the multi-processor setting. Our main result is an enhanced analysis of a simple non-migratory online algorithm called CRR (classified round robin) on $m \ge 2$ processors, showing that its flow time plus energy is within O(1) times of the optimal non-migratory offline algorithm, when the maximum allowable speed is slightly relaxed. The result still holds even if the comparison is made against the optimal migratory offline algorithm. This improves previous analysis that CRR is $O(\log P)$ -competitive where P is the ratio of the maximum job size to the minimum job size.

1 Introduction

Energy consumption has become a key issue in the design of modern processors. This is essential not only for battery-operated mobile devices with single processor but also for server farms or laptops with multi-core processors. A popular technology to reduce energy usage is *dynamic speed scaling* (see, e.g., [8, 14, 24, 28]) where the processor can vary its speed dynamically. Running a job at a slower speed is more energy efficient, yet it takes longer time and may affect the performance. In the past few years, a lot of effort has been devoted to revisiting classical scheduling problems with dynamic speed scaling and energy concern taken into consideration (e.g., [1, 2, 6, 7, 9, 10, 16, 26, 29]; see [15] for a survey). The challenge basically arises from the conflicting objectives of providing good "quality of service" (QoS) and conserving energy.

One commonly used QoS measurement for scheduling jobs on a processor is the total flow time (or equivalently, average response time). Here, jobs with arbitrary size are released at unpredictable times and the flow time of a job is the time elapsed since it arrives until it is completed. When energy is not a concern, the objective of a scheduler is simply to minimize the total flow time of all jobs, and it is well known that the online algorithm SRPT (shortest remaining processing time first) produces a schedule with the smallest possible flow time. The study of energy-efficient

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scheduling was initiated by Yao, Demers and Shenker [29]. They considered deadline scheduling in the model where the processor can run at any speed between 0 and ∞ , and incurs an energy of s^{α} per unit time when running at speed s, where $\alpha \geq 2$ (typically 2 or 3 [8,22]). This model, which we call unbounded speed model, also paves the way for studying scheduling that minimizes both the flow time and energy. In particular, Pruhs et al. [27] studied offline scheduling for minimizing the total flow time on a single processor with a given amount of energy. They gave a polynomial time optimal algorithm for the special case when jobs are of unit size. However, this problem does not admit any constant competitive online algorithm even if jobs are of unit size [7].

Flow time and energy. To better understand the tradeoff between flow time and energy, Albers and Fujiwara [1] proposed combining the dual objectives into a single objective of minimizing the sum of total flow time and energy. The intuition is that, from an economic viewpoint, it can be assumed that users are willing to pay a certain units (say, ρ units) of energy to reduce one unit of flow time. By changing the units of time and energy, one can further assume that $\rho = 1$ and thus would like to optimize total flow time plus energy. Albers and Fujiwara presented an online algorithm that is $8.3e(\frac{3+\sqrt{5}}{2})^{\alpha}$ -competitive for jobs of unit size. This result was later improved by Bansal, Pruhs, and Stein [7], who gave a 4-competitive algorithm for jobs of unit size. They also considered the more important case of arbitrary job size and weight, and presented the first online algorithm (denoted as BPS below) that is O(1)-competitive for flow time plus energy. More recently, Lam et al. [19] devised a simple speed scaling algorithm AJC, which, when coupled with SRPT, improves the competitive ratio to $2/(1 - \frac{\alpha-1}{\alpha^{\alpha/(\alpha-1)}}) < 2\alpha/\ln \alpha$. For example, if $\alpha = 3$, the ratios of BPS and SRPT-AJC are about 7.94 and 3.25, respectively.

The unbounded speed model has provided a convenient model for studying power management, yet it is not realistic to assume unbounded speed. Recently, Chan et al. [10] introduced the *bounded speed model*, where the speed can be scaled between 0 and some maximum T. Bansal et al. [5] adapted the previous results on minimizing flow time plus energy [7] to this model. For jobs of arbitrary size and weight, the BPS algorithm is still O(1)-competitive when using a processor with maximum speed $(1+\epsilon)T$ for any $\epsilon > 0$ (precisely, the competitive ratio is $\max\{(1+1/\epsilon), (1+\epsilon)^{\alpha}\}(2+o(1))\alpha/\ln \alpha$). It is worth-mentioning that BPS also relies on extra speed in the unbounded speed model, but the extra speed is absorbed by the model implicitly. SRPT-AJC works in the bounded speed model as well, leading to a smaller competitive ratio of $\beta = 2(\alpha + 1)/(\alpha - \frac{\alpha - 1}{(\alpha + 1)^{1/(\alpha - 1)}}) < 2\alpha/\ln \alpha$. More interestingly, SRPT-AJC does not demand a processor with higher maximum speed. For example, if $\alpha = 3$, the ratios of BPS and SRPT-AJC can be 11.91 (using maximum speed 1.466T) and 4, respectively.

Multi-processor scheduling. All the results above are about single-processor scheduling. In the older days, when energy was not a concern, flow time scheduling on multiple processors running at fixed speed was an interesting problem by itself (e.g., [3, 4, 12, 13, 20, 25]). The study of multi-processor scheduling is not only of theoretical interest. In fact, modern computers are using a multi-core processor, which is essentially a pool of identical processors.

In the multi-processor setting, jobs remain sequential in nature and cannot be executed by more than one processor in parallel. We distinguish schedules that would migrate jobs among processors from those that would not. Different online algorithms like SRPT and IMD [3] that are $\Theta(\log P)$ -competitive have been proposed respectively under the migratory and the non-migratory model, where P is the ratio of the maximum job size to the minimum job size [3,4,20]. Furthermore, Chekuri et al. [12] have shown that IMD is $O(1+1/\epsilon)$ -competitive when using processors $(1+\epsilon)$ times

Flow time	Flow time plus energy
$\Theta(\log P)$ [3,4]	$O(\log P)$ using
	$(1 + \epsilon)$ -speed processors [18]
O(1) using	O(1) using
$(1+\epsilon)$ -speed processors [12]	$(1 + \epsilon)$ -speed processors [this paper]

Table 1: The competitive ratios of non-migratory online algorithms for minimizing flow time and flow time plus energy.

faster. If migration is allowed, SRPT can achieve a competitive ratio one or even smaller, when using sufficiently fast processors [21,23]. It is worth-mentioning that non-migratory algorithms are preferred in practice because migrating jobs requires overheads and is avoided in many applications.

Bunde [9] is the first to study multi-processor scheduling that takes both flow time and energy into consideration, his work is about offline approximation for jobs of unit size. Recently, Lam et al. [18] gave the first online algorithm for minimizing flow time plus energy for jobs of arbitrary size; their algorithm is non-migratory and is $O(\log P)$ -competitive when using processors with slightly higher maximum speed. They also showed an $\Omega(\log P)$ lower bound if extra speed is not allowed. The literature also contains multi-processor results on optimizing other classical objectives together with energy.¹

To save energy in the multi-processor setting, one would like to balance the load of the processors so as to avoid running any processor at high speed, and it is natural to consider some kind of roundrobin strategy. Lam et al. [18] in particular considered the online algorithm CRR (classified round robin), which divides jobs into different classes according to their sizes and dispatches jobs of the same class to the processors in a round-robin fashion. They showed that CRR can lead to an $O(\log P)$ -competitive algorithm for flow time plus energy. In the bounded speed model, this algorithm requires the processors to have maximum speed $(1 + \epsilon)T$ for any $\epsilon > 0$. As mentioned before, when flow time is the only concern, $(1 + \epsilon)$ times faster processors can lead to O(1)competitive algorithm even if the online algorithm is required to be non-migratory [12]. Thus, it is natural to ask whether there exists an O(1)-competitive algorithm for flow time plus energy when extra speed is allowed.

Our contribution. In this paper, we give an enhanced analysis of CRR for optimizing flow time plus energy on $m \ge 2$ processors, and show that CRR can be O(1)-competitive when using processors with slightly higher maximum speed. Table 1 shows a summary of non-migratory results on scheduling for flow time with or without energy concern.

The core of our analysis is to prove that there always exists a non-migratory schedule that dispatches jobs according to CRR and whose flow time plus energy is within a constant factor (instead of a factor of $O(\log P)$) of the optimal migratory offline schedule. As a result, we can dispatch jobs in an online fashion by using CRR. And we can approximate the optimal schedule of each processor by using the online algorithm BPS [5,7] or SRPT-AJC [19] separately for each processor. Since these algorithms are O(1)-competitive for minimizing flow time plus energy in the single-processor setting, they can support CRR to give a competitive result for the multi-processor setting. The performance of CRR plus SRPT-AJC, denoted as CRR-SA, in the bounded speed

¹Pruhs et al. [26] and Bunde [9] both studied offline algorithms for the makespan objective. Albers et al. [2] studied online algorithms for scheduling jobs with restricted deadlines.

model (where T denotes the maximum speed) is summarized as below.² It is convenient to define a constant $\eta_{\epsilon} = (1+\epsilon)^{\alpha}[(1+\epsilon)^{\alpha-1} + (1-1/\alpha)(2+\epsilon)/\epsilon^2]$ for any $\epsilon > 0$. Following [19], β is defined as $2(\alpha+1)/(\alpha-\frac{\alpha-1}{(1+\alpha)^{1/(\alpha-1)}})$.

- Against the optimal (offline) non-migratory schedule: For any $\epsilon > 0$, CRR-SA can be $(2\eta_{\epsilon}\beta)$ competitive for minimizing flow time plus energy, when the maximum allowable speed is
 relaxed to $(1 + \epsilon)^2 T$. E.g., if $\alpha = 2$ and $\epsilon = 0.6$, the competitive ratio is about 96; if $\alpha = 3$ and $\epsilon = 0.1$, the competitive ratio is 1504.
- Against the optimal (offline) migratory schedule: The competitive ratio becomes $5\eta_{\epsilon}\beta$.

Both the analyses in [18] and in this paper stem from an offline result to eliminate migration.³ Roughly speaking, the analysis in [18] relies on a transformation of an arbitrary job set to an "*m*parallel" job set, in which jobs can be divided into groups of *m* jobs with same release time. For *m*-parallel job set, it is relatively easy to obtain an optimal non-migratory schedule that dispatches jobs according to CRR using extra speed. Yet transforming an arbitrary job set to an *m*-parallel job set and vice versa require a non-trivial technique to modify the release times of jobs forward and backward. Such transformation increases the flow time plus energy of the resulting schedule by a factor of $O(\log P)$, and it cannot be improved by using extra speed. In this paper, instead of relying on such transformation, we have an observation that we can focus on some special schedules called "immediate-start" schedules, from which we can derive a much simpler algorithm to construct CRR schedules and exploit the extra speed to show that the flow time plus energy increases by only a constant factor (rather than an $O(\log P)$ factor).

Remarks for fixed-speed scheduling. The analysis of CRR also reveals its performance in the context of traditional flow-time scheduling, where processors are of fixed-speed and the concern is on flow time only. In this case, CRR (plus SRPT for individual processor) would give a non-migratory online algorithm which, when compared with the optimal migratory algorithm, can have a competitive ratio of one or even any constant arbitrarily smaller than one, when using sufficiently fast processors. The competitive ratio can be (56.72/s) when using s-speed processors for $s \geq 56.72$.⁴ Note that if migration is allowed, the efficiency can be much better as McCullough and Torng [21] have showed that the migratory algorithm SRPT is $\frac{1}{s}$ -competitive when using s-speed processors, where $s \geq 2 - \frac{1}{m}$.

1.1 Preliminaries

Definitions and notations. Given a job set \mathcal{J} , we want to schedule \mathcal{J} on a pool of $m \geq 2$ processors. Note that jobs are sequential in nature and cannot be executed by more than one processor in parallel. All processors are identical and a job can be executed on any processor. Preemption is allowed and a preempted job can be resumed at the point of preemption. We differentiate two

 $^{^{2}}$ Our analysis can also be applied to the unbounded speed model, but it is of less interest.

³The cost of eliminating migration has been investigated in the classical setting such as deadline scheduling [11,17] and flow time scheduling [3,4].

⁴Precisely, we can show that for any $\epsilon > 0$ such that $\frac{5}{\epsilon^2}(2+\epsilon) \ge 1$, CRR using processors of speed $(1+\epsilon)^2$ is $\frac{5}{\epsilon^2}(2+\epsilon)$ -competitive. Combining with the result of McCullough and Torng [21] that SRPT is $\frac{1}{s}$ -competitive for single processor when using processor of speed s, this implies CRR using processors of speed $s(1+\epsilon)^2$ is $\frac{5}{s\epsilon^2}(2+\epsilon)$ -competitive. By changing variables, we can show that with $\sigma = 56.72$, CRR using processors of speed $s \ge \sigma$ is $\frac{5\sigma(\sqrt{\sigma}+1)}{s(\sqrt{\sigma}-1)^2}$ -competitive, and $\frac{5\sigma(\sqrt{\sigma}+1)}{(\sqrt{\sigma}-1)^2} \approx 56.72$.

types of schedules: a migratory schedule can move partially-executed jobs from one processor to another processor without any penalty, and a non-migratory schedule cannot.

We use r(j) and p(j) to denote respectively the release time and work requirement (or size) of job j. We let $p(\mathcal{J}) = \sum_{j \in \mathcal{J}} p(j)$ be the total size of \mathcal{J} . The time required to complete job j using a processor with fixed speed s is p(j)/s.

With respect to a schedule S for a job set \mathcal{J} , we use the notations max-speed(S), $E_{\mathcal{J}}(S)$ and $F_{\mathcal{J}}(S)$ to denote the maximum speed, energy usage, and total flow time, respectively. It is convenient to define $G_{\mathcal{J}}(S) = F_{\mathcal{J}}(S) + E_{\mathcal{J}}(S)$. When the context is clear, we will omit the subscript \mathcal{J} . Note that F(S) is the sum, over all jobs, of the time since a job is released until it is completed, or equivalently, the integration over time of the number of unfinished jobs. As processor speed can vary dynamically and the time to execute a job j is not necessarily equal to p(j), we define the *execution time* of job j to be the flow time minus the waiting time of j, and define the total execution time of S to be the sum of the execution time over all jobs in S.

Properties of *m***-processor schedules.** The following lemma shows a lower bound on G(S) which depends on $p(\mathcal{J})$, irrelevant of the number of processors.

Lemma 1. For any *m*-processor schedule S for a job set \mathcal{J} , $G(S) \geq \frac{\alpha}{(\alpha-1)^{1-1/\alpha}} p(\mathcal{J})$.

Proof. Suppose that a job j in S has flow time t. The energy usage for j is minimized if j is run at constant speed p(j)/t throughout, and it is at least $(p(j)/t)^{\alpha}t = p(j)^{\alpha}/t^{\alpha-1}$. Since $t + p(j)^{\alpha}/t^{\alpha-1}$ is minimized when $t = (\alpha - 1)^{1/\alpha}p(j)$, we have $t + p(j)^{\alpha}/t^{\alpha-1} \ge \frac{\alpha}{(\alpha-1)^{1-1/\alpha}}p(j)$. Summing over all jobs, we obtain the desired lower bound.

We also observe the following property of any optimal schedule.

Lemma 2. Any optimal schedule runs a job at the same speed throughout its lifespan.

Proof. This is due to the convexity of the power function s^{α} . In any migratory or non-migratory schedule, if a job j is not run at the same speed throughout its lifespan, we can always reduce the energy consumption as follows. Let I be the set of time intervals during which j is run, and let s(t) be the speed that j is run at time t (note that j is processed by at most one processor at t). By Jensen's Inequality, $\int_{t \in I} s(t)^{\alpha} dt > |I| \cdot (\int_{t \in I} s(t) dt/|I|)^{\alpha}$, i.e., running j at the average speed over the intervals in I reduces the energy.

Critical speed. Without loss of generality, we can assume that at any time an optimal schedule never runs a job at speed less than the critical speed, defined as $1/(\alpha - 1)^{1/\alpha}$ [1], and the maximum speed T is at least the critical speed. The assumption stems from an observation (Lemma 4) that a multi-processor schedule can be transformed without increasing the flow time plus energy so that it never runs a job j at speed less than the critical speed. Lemma 4 makes use of a result by Albers and Fujiwara [1] (Lemma 3) that when scheduling a single job j on a single processor for minimizing total flow time plus energy, j should be executed at the critical speed, i.e., $1/(\alpha - 1)^{1/\alpha}$.

Lemma 3. [1] At any time after a job j has been run on a processor for a while, suppose that we want to further execute j for another x > 0 units of work and minimize the flow time plus energy incurred in this period. The optimal strategy is to let the processor always run at the critical speed.

Lemma 4. Given any m-processor schedule S for a job set \mathcal{J} , we can construct an m-processor schedule S' for \mathcal{J} such that S' never runs a job at speed less than the critical speed and $G(S') \leq G(S)$. Moreover, S' needs migration if and only if S does; and max-speed(S') is at most max{max-speed(S), $1/(\alpha - 1)^{1/\alpha}$ }.

Proof. Assume that there is a time interval I in S during which a processor i is running a job j below the critical speed. If S needs migration, we transform S to a migratory schedule S_1 for \mathcal{J} such that job j is always scheduled in processor i. This can be done by swapping the schedules of processor i and other processors for different time intervals. If S does not need migration, job j is entirely scheduled in processor i and S_1 is simply S. In both cases, $G(S_1) = G(S)$.

We can then improve $G(S_1)$ by modifying the schedule of processor i as follows. Let x be the amount of work of j processed during I on processor i. First, we schedule this amount of work of j at the critical speed. Note that the time required is shortened. Then we move the remaining schedule of j backward to fill up the time shortened. By Lemma 3, the flow time plus energy for j is preserved. Other jobs in \mathcal{J} are left intact. To obtain the schedule S', we repeat this process to eliminate all such intervals I.

We can assume that the maximum speed T is at least the critical speed. Otherwise, any multiprocessor schedule including the optimal one would always run a job at the maximum speed. It is because when running a job below the critical speed, the slower the speed, the more total flow time plus energy is incurred. In other words, the problem is reduced to minimizing flow time alone.

2 The online algorithm

This section presents the definition of the online algorithm CRR-SA, which produces a nonmigratory schedule for $m \ge 2$ processors. The following three sections are devoted to proving that, when compared with the optimal non-migratory or migratory schedule, this algorithm is O(1)-competitive for flow time plus energy if the maximum allowable speed is slightly relaxed.

Consider any fixed constant $\epsilon > 0$. A job is said to be in class k if its size is in the range $((1+\epsilon)^{k-1}, (1+\epsilon)^k]$. In a $CRR(\epsilon)$ schedule, jobs of the same class are dispatched upon their arrival to the m processors using a round-robin strategy, and different classes are handled independently. Jobs once dispatched to a processor will be processed there until they finish. Thus a $CRR(\epsilon)$ schedule is non-migratory in nature.

The intuition of using a CRR schedule comes from a new offline result that there is a CRR schedule such that the total flow time plus energy is O(1) times that of the optimal (non-migratory or migratory) offline schedule and the maximum allowable speed is only slightly higher. Details are stated in Theorem 5 below (Sections 3, 4 and 5 are devoted to proving this theorem). Recall that the constant η_{ϵ} is defined as $(1 + \epsilon)^{\alpha} [(1 + \epsilon)^{\alpha-1} + (1 - 1/\alpha)(2 + \epsilon)/\epsilon^2]$.

Theorem 5. Given a job set \mathcal{J} , let \mathcal{O}_1 and \mathcal{O}_2 be respectively an optimal non-migratory schedule and an optimal migratory schedule for \mathcal{J} using maximum speed T. Then, for any $\epsilon > 0$,

- *i.* we can construct from \mathcal{O}_1 a $CRR(\epsilon)$ schedule \mathcal{S}_1 for \mathcal{J} such that $G(\mathcal{S}_1) \leq 2\eta_{\epsilon}G(\mathcal{O}_1)$, and $max-speed(\mathcal{S}_1) \leq (1+\epsilon)^2 \times max-speed(\mathcal{O}_1)$; and
- ii. we can construct from \mathcal{O}_2 a $CRR(\epsilon)$ schedule \mathcal{S}_2 for \mathcal{J} such that $G(\mathcal{S}_2) \leq 5\eta_{\epsilon}G(\mathcal{O}_2)$, and max-speed $(\mathcal{S}_2) \leq (1+\epsilon)^2 \times max$ -speed (\mathcal{O}_2) .

Notice that the CRR schedule stated in Theorem 5 cannot be constructed online. Nevertheless, with Theorem 5, it is natural to design an online algorithm that first dispatches jobs using the policy $CRR(\epsilon)$, and then schedules jobs on each processor independently in a way that is competitive in the single-processor setting. For the latter we make use of the single-processor algorithm SRPT-AJC [19]. Below we review the algorithm SRPT-AJC and define the multi-processor algorithm CRR-SA. The definition of SRPT-AJC is for reference only, in this paper we only need to know its performance.

Algorithm SRPT-AJC. At any time, run the job with the smallest remaining work at speed $n^{1/\alpha}$, where *n* denotes the number of unfinished jobs at the current time. If $n^{1/\alpha}$ exceeds the maximum allowable speed *T*, just use the maximum speed *T*.

Algorithm CRR-SA. Jobs are dispatched to the *m* processors with the $CRR(\epsilon)$ policy. Jobs in each processor are scheduled independently using SRPT-AJC.

For a single processor, SRPT-AJC performs well in minimizing flow time plus energy.

Lemma 6. [19] For minimizing flow time plus energy of a single processor in the bounded speed model, SRPT-AJC is β -competitive, where $\beta = 2(\alpha + 1)/(\alpha - \frac{\alpha - 1}{(1+\alpha)^{1/(\alpha-1)}})$.

Analysis of CRR-SA. With Theorem 5 and Lemma 6, we can easily derive the performance of CRR-SA against the optimal non-migratory or migratory algorithm.

Corollary 7. In the bounded speed model, the performance of CRR-SA for minimizing flow time plus energy on $m \ge 2$ processors is as follows. For any $\epsilon > 0$,

- i. Against a non-migratory optimal schedule, CRR-SA is $2\eta_{\epsilon}\beta$ -competitive when using processors with maximum speed relaxed to $(1 + \epsilon)^2 T$.
- ii. Against a migratory optimal schedule, CRR-SA is $5\eta_{\epsilon}\beta$ -competitive when using processors with maximum speed relaxed to $(1 + \epsilon)^2 T$.

Proof. Let \mathcal{O} be the optimal non-migratory schedule for a job set \mathcal{J} . Consider any $\epsilon > 0$. By Theorem 5 (i), there exists a $\operatorname{CRR}(\epsilon)$ schedule \mathcal{S} for \mathcal{J} such that $G(\mathcal{S}) \leq 2\eta_{\epsilon}G(\mathcal{O})$ and $max-speed(\mathcal{S}) \leq (1+\epsilon)^2 \times max-speed(\mathcal{O})$. Let \mathcal{S}' be the $\operatorname{CRR}(\epsilon)$ schedule produced by $\operatorname{CRR-SA}$ for \mathcal{J} . Applying Lemma 6 to individual processors, we conclude that $G(\mathcal{S}') \leq \beta G(\mathcal{S}) \leq 2\eta_{\epsilon}\beta G(\mathcal{O})$, and $max-speed(\mathcal{S}') \leq (1+\epsilon)^2 \times max-speed(\mathcal{O})$. The analysis of the migratory case is the same. \Box

3 Restricted but useful optimal schedules

The remaining three sections are devoted to proving Theorem 5. In essence, for any $\epsilon > 0$, we want to construct a $CRR(\epsilon)$ schedule from an optimal schedule with a mild increase in flow time plus energy and in maximum speed. In this section, we introduce two notions to restrict the possible optimal (non-migratory or migratory) schedules so as to ease the construction.

- A job set \mathcal{J} is said to be *power-of-*(1 + ϵ) if every job in \mathcal{J} has size $(1 + \epsilon)^k$ for some k.
- For any job set \mathcal{J} and schedule \mathcal{S} , we say that \mathcal{S} is *immediate-start* if every job starts at exactly its release time in \mathcal{J} .

In general, an optimal schedule may not be immediate-start. The rest of this section shows that it suffices to focus on job sets that are power-of- $(1 + \epsilon)$ and admit optimal schedules that are also immediate-start (such schedules will be referred to as *immediate-start, optimal schedules*). See Corollary 10 below for a technical summary. The job size restriction is relatively easy to observe as we can exploit a slightly higher maximum speed (Lemma 8). The immediate-start property is non-trivial and perhaps counter-intuitive (Lemma 9).

Technically speaking, the results below (Lemmas 8, 9 and Corollary 10) hold in both the migratory and the non-migratory setting. To simplify the presentation of this section, we will not mention whether schedules are migratory or non-migratory. One should read the lemmas and proofs by assuming all schedules are either migratory or non-migratory.

Lemma 8. Given a job set \mathcal{J} , we can construct a power-of- $(1 + \epsilon)$ job set \mathcal{J}' such that

- i. any schedule S_1 for \mathcal{J} defines a schedule S'_1 for \mathcal{J}' such that $G(S'_1) \leq (1+\epsilon)^{\alpha} G(S_1)$ and max-speed $(S'_1) \leq (1+\epsilon) \times max$ -speed (S_1) ; and
- ii. any schedule S'_2 for \mathcal{J}' defines a schedule S_2 for \mathcal{J} with $G(S_2) \leq G(S'_2)$ and max-speed $(S_2) = max$ -speed (S'_2) .

Proof. \mathcal{J}' can be constructed from \mathcal{J} by rounding up the size of each job in \mathcal{J} to the nearest power of $(1 + \epsilon)$. (i) \mathcal{S}_1 naturally defines a schedule \mathcal{S}'_1 for \mathcal{J}' as follows. Whenever \mathcal{S}_1 runs a job j at speed s, \mathcal{S}'_1 runs the corresponding job in \mathcal{S}' at speed $s' = s \times (1 + \epsilon)^{\lceil \log_{1+\epsilon} p(j) \rceil} / p(j)$. Note that $s' \leq (1 + \epsilon)s$, $E(\mathcal{S}'_1) \leq (1 + \epsilon)^{\alpha} E(\mathcal{S}_1)$, and $F(\mathcal{S}'_1) = F(\mathcal{S}_1)$. (ii) is obvious as we can apply any schedule \mathcal{S}'_2 for \mathcal{J}' to schedule \mathcal{J} with extra idle time.

We now explain why we can focus on optimal schedules that are immediate-start. Unless otherwise stated, an optimal schedule \mathcal{O} below means a schedule that has the smallest flow time plus energy among all schedules with maximum speed not exceeding *max-speed*(\mathcal{O}). To ease the discussion, we add a subscript \mathcal{J} to the notations F, E, and G to denote that the job set under concern is \mathcal{J} . Given a power-of- $(1 + \epsilon)$ job set \mathcal{J}_1 and an optimal schedule \mathcal{O}_1 for \mathcal{J}_1 , Algorithm MAKEISO constructs a power-of- $(1 + \epsilon)$ job set \mathcal{J}_2 and an immediate-start, optimal schedule \mathcal{O}_2 for \mathcal{J}_2 and Lemma 9 states the properties of the constructed schedule.

Algorithm MAKEISO. We first construct \mathcal{O}_2 from \mathcal{J}_1 and \mathcal{O}_1 . The idea is to repeatedly pick two jobs of the same size and swap their schedules in \mathcal{O}_1 . More specifically, each time we consider all jobs in \mathcal{J}_1 of a particular size, and swap their schedules so that their release times and start times in \mathcal{O}_2 are in the same order (note that the speed at any time stays unchanged and ties are broken by job ids). That is, for all *i*, the job with the *i*-th smallest release time will take up the schedule of the job with the *i*-th smallest start time; note that the *i*-th smallest start time can never be earlier than the *i*-th smallest release time. Thus, \mathcal{O}_2 is also a valid schedule for \mathcal{J}_1 .

Next, we modify \mathcal{J}_1 to \mathcal{J}_2 , by replacing the release time of each job j with its start time in \mathcal{O}_2 . Note that the release time of j can only be delayed (and never gets advanced). Any schedule for \mathcal{J}_2 (including \mathcal{O}_2) is also a valid schedule for \mathcal{J}_1 .

We now show the following properties of the job set and the schedule constructed by MAKEISO.

Lemma 9. Given a power-of- $(1 + \epsilon)$ job set \mathcal{J}_1 and an optimal schedule \mathcal{O}_1 for \mathcal{J}_1 , Algorithm MAKEISO constructs a power-of- $(1 + \epsilon)$ job set \mathcal{J}_2 and an immediate-start, optimal schedule \mathcal{O}_2 for \mathcal{J}_2 with max-speed(\mathcal{O}_2) \leq max-speed(\mathcal{O}_1). Furthermore, any $CRR(\epsilon)$ schedule \mathcal{S}_2 for \mathcal{J}_2 defines a $CRR(\epsilon)$ schedule \mathcal{S}_1 for \mathcal{J}_1 , and if $G_{\mathcal{J}_2}(\mathcal{S}_2) \leq \gamma G_{\mathcal{J}_2}(\mathcal{O}_2)$ for some $\gamma \geq 1$, then $G_{\mathcal{J}_1}(\mathcal{S}_1) \leq \gamma G_{\mathcal{J}_1}(\mathcal{O}_1)$.

Proof. By construction, \mathcal{O}_2 is an immediate-start schedule for \mathcal{J}_2 . We now analyze the relationship between \mathcal{O}_1 and \mathcal{O}_2 and show that CRR preserves performance.

 \mathcal{O}_1 and \mathcal{O}_2 incur the same flow time plus energy for \mathcal{J}_1 . Since \mathcal{O}_1 and \mathcal{O}_2 use the same speed at any time, $E_{\mathcal{J}_1}(\mathcal{O}_1) = E_{\mathcal{J}_1}(\mathcal{O}_2)$. Furthermore, at any time, \mathcal{O}_1 completes a job if and only if \mathcal{O}_2 completes a (possibly different) job, and thus \mathcal{O}_1 and \mathcal{O}_2 always have the same number of unfinished jobs. This means that $F_{\mathcal{J}_1}(\mathcal{O}_1) = F_{\mathcal{J}_1}(\mathcal{O}_2)$ and $G_{\mathcal{J}_1}(\mathcal{O}_1) = G_{\mathcal{J}_1}(\mathcal{O}_2)$.

 \mathcal{O}_2 is optimal for \mathcal{J}_2 (in terms of flow time plus energy). Suppose on the contrary that there is a schedule \mathcal{O}' for \mathcal{J}_2 with $G_{\mathcal{J}_2}(\mathcal{O}') < G_{\mathcal{J}_2}(\mathcal{O}_2)$. Any schedule for \mathcal{J}_2 , including \mathcal{O}' and \mathcal{O}_2 , is also a valid schedule for \mathcal{J}_1 . Note that $E_{\mathcal{J}_1}(\mathcal{O}') = E_{\mathcal{J}_2}(\mathcal{O}')$, and $F_{\mathcal{J}_1}(\mathcal{O}') = F_{\mathcal{J}_2}(\mathcal{O}') + d$, where d is the total delay of release times of all jobs in \mathcal{J}_2 (when comparing with \mathcal{J}_1). Therefore, $G_{\mathcal{J}_1}(\mathcal{O}') = G_{\mathcal{J}_2}(\mathcal{O}') + d$, and similarly for \mathcal{O}_2 . Thus, if $G_{\mathcal{J}_2}(\mathcal{O}') < G_{\mathcal{J}_2}(\mathcal{O}_2)$, then

$$G_{\mathcal{J}_1}(\mathcal{O}') = G_{\mathcal{J}_2}(\mathcal{O}') + d$$

$$< G_{\mathcal{J}_2}(\mathcal{O}_2) + d = G_{\mathcal{J}_1}(\mathcal{O}_2) = G_{\mathcal{J}_1}(\mathcal{O}_1) .$$

This contradicts the optimality of \mathcal{O}_1 for \mathcal{J}_1 .

CRR preserves performance. Consider any $\operatorname{CRR}(\epsilon)$ schedule S_2 for \mathcal{J}_2 satisfying $G_{\mathcal{J}_2}(S_2) \leq \gamma G_{\mathcal{J}_2}(\mathcal{O}_2)$, for some $\gamma \geq 1$. By definition, jobs of the same class are also of same size and have the same order of release times in \mathcal{J}_1 and \mathcal{J}_2 . Therefore, S_2 is also an $\operatorname{CRR}(\epsilon)$ schedule for \mathcal{J}_1 . For total flow time plus energy,

$$G_{\mathcal{J}_1}(\mathcal{S}_2) = G_{\mathcal{J}_2}(\mathcal{S}_2) + d \le \gamma G_{\mathcal{J}_2}(\mathcal{O}_2) + d$$

$$\le \gamma (G_{\mathcal{J}_2}(\mathcal{O}_2) + d) = \gamma G_{\mathcal{J}_1}(\mathcal{O}_2) = \gamma G_{\mathcal{J}_1}(\mathcal{O}_1) .$$

Thus the lemma follows.

In summary, the two lemmas above allow us to focus on power-of- $(1 + \epsilon)$ job sets that admit immediate-start, optimal schedules.

Corollary 10. Let \mathcal{J} be a job set and let \mathcal{O} be an optimal schedule for \mathcal{J} . For any $\epsilon > 0$, there exists a power-of- $(1 + \epsilon)$ job set \mathcal{J}' and a schedule \mathcal{O}' for \mathcal{J}' that is immediate-start and optimal among all schedules with maximum speed $(1 + \epsilon) \times \max$ -speed (\mathcal{O}) . Furthermore, any $CRR(\epsilon)$ schedule \mathcal{S}' for \mathcal{J}' defines a $CRR(\epsilon)$ schedule \mathcal{S} for \mathcal{J} , and if $G_{\mathcal{J}'}(\mathcal{S}') \leq \gamma G_{\mathcal{J}'}(\mathcal{O}')$ for some $\gamma \geq 1$, then $G_{\mathcal{J}}(\mathcal{S}) \leq \gamma (1 + \epsilon)^{\alpha} G_{\mathcal{J}}(\mathcal{O})$.

Proof. By Lemma 8 (i), we construct from \mathcal{J} and \mathcal{O} a power-of- $(1 + \epsilon)$ job set \mathcal{J}_1 and a schedule \mathcal{S}_1 for \mathcal{J}_1 with $G_{\mathcal{J}_1}(\mathcal{S}_1) \leq (1 + \epsilon)^{\alpha} G_{\mathcal{J}}(\mathcal{O})$ and max-speed $(\mathcal{S}_1) \leq (1 + \epsilon) \times max$ -speed (\mathcal{O}) . Let \mathcal{O}_1 be the optimal schedule for \mathcal{J}_1 with maximum speed $(1 + \epsilon) \times max$ -speed (\mathcal{O}) . Then $G_{\mathcal{J}_1}(\mathcal{O}_1) \leq G_{\mathcal{J}_1}(\mathcal{S}_1) \leq (1 + \epsilon)^{\alpha} G_{\mathcal{J}}(\mathcal{O})$. Next we apply Lemma 9 to \mathcal{J}_1 and \mathcal{O}_1 , and we obtain \mathcal{J}' and an immediate start, optimal schedule \mathcal{O}' with maximum speed at most max-speed (\mathcal{O}_1) , which is at most $(1 + \epsilon)max$ -speed (\mathcal{O}) . Furthermore, by Lemma 9 and Lemma 8 (ii), \mathcal{S}' defines a CRR (ϵ) schedule \mathcal{S} for \mathcal{J} such that if $G_{\mathcal{J}'}(\mathcal{S}') \leq \gamma G_{\mathcal{J}'}(\mathcal{O}')$ for some $\gamma \geq 1$, then $G_{\mathcal{J}}(\mathcal{S}) \leq \gamma G_{\mathcal{J}_1}(\mathcal{O}_1) \leq \gamma(1 + \epsilon)^{\alpha}G_{\mathcal{J}}(\mathcal{O})$.

In the rest of this paper, we further exploit the fact that any optimal schedule runs a job at the same speed throughout its lifespan (Lemma 2). Also, without loss of generality, at any time an optimal schedule never runs a job at speed less than the critical speed, defined as $1/(\alpha - 1)^{1/\alpha}$ [1], and the maximum speed T is at least the critical speed (see Section 1.1 for justification).

4 Constructing CRR schedules

This section presents an algorithm, called MAKECRR, to construct a CRR schedule from an optimal non-migratory or migratory schedule S^* for a job set \mathcal{J} . By Corollary 10, we focus on the case where \mathcal{J} consists of power-of- $(1 + \epsilon)$ jobs only and S^* is immediate-start. Note that in a CRR(ϵ) schedule for \mathcal{J} , jobs in each class are of identical size, and the round robin policy is effectively applied independently to every subset of jobs of the same size. It is non-trivial to prove that MAKECRR only increases the flow time and energy of the schedule by a moderate amount.

Before we detail the algorithm, it is useful to observe the nature of the CRR schedule S to be constructed. The ordering of job execution in S could be very different from S^* . Roughly speaking, S only makes reference to the speed used by S^* . Recall that in S^* , a job is run at the same speed throughout its lifespan. For any job, S determines its speed as the average of a certain subset of (b+1)m jobs in S^* , where b = 1 or 4 depending on whether S^* is non-migratory or migratory. The constant b arises from an upper bound of the number of jobs of the same size that have started but not yet finished at any time. We assume that the processors are numbered from 0 to m - 1.

Algorithm MAKECRR. The algorithm has a parameter $\lambda > 0$ to control the extra speed (we will eventually set $\lambda = \epsilon$ to derive the desired result).

The construction runs in multiple rounds, from the smallest job size to the largest. Let S_0 denote the intermediate schedule, which is initially empty and eventually becomes S. We modify S_0 in each round to include more jobs. In the round for size p, suppose that \mathcal{J} contains n jobs $\{j_1, j_2, \ldots, j_n\}$ of size p, arranged in increasing order of release times. It is convenient to define $j_{n+1} = j_1, j_{n+2} = j_2$, etc. For i = 1 to n, let x_i be the average speed in S^* of the fastest m jobs among the following (b+1)m jobs: $j_i, j_{i+1}, \ldots, j_{i+(b+1)m-1}$. We modify S_0 by adding a schedule for j_i in processor $(i \mod m)$: it can start as early as at its release time, runs at constant speed $(1+\lambda)x_i$, and occupies the earliest possible times, while avoiding times already committed to earlier jobs for processor $(i \mod m)$.

Performance of the constructed schedule S. To study the performance of S constructed by Algorithm MAKECRR, we need to be very specific about the properties of the job set \mathcal{J} and the optimal schedule S^* . By Corollary 10, we assume that \mathcal{J} consists of power-of- $(1 + \epsilon)$ jobs only and S^* is immediate-start. Furthermore, if S^* is non-migratory, it is useful to observe the following property.

Property 11. Consider any optimal non-migratory schedule for \mathcal{J} on $m \geq 2$ processors. At any time, for each job size, there are at most m jobs which have started but not yet finished. We call this property m-proceeding.

Property 11 holds because in any optimal non-migratory schedule (no matter whether it is immediate-start or not), jobs of the same size dispatched to a processor must work in a First-Come-First-Serve manner. Otherwise we can shuffle the execution order to First-Come-First-Serve and reduce the total flow time, and the schedule is not optimal.

Note that the *m*-proceeding property may not hold for an optimal migratory schedule. Nevertheless, we observe a weaker property. In Section 5, we will prove that there exists an optimal migratory schedule for \mathcal{J} that is 4*m*-proceeding and immediate-start.

Now we are ready to state the performance of the schedule S constructed by Algorithm MAKE-CRR. Roughly speaking, if S^* is immediate-start and *m*-proceeding (or 4*m*-proceeding), then S is a CRR schedule with comparable performance. Details are as follows. It is useful to define $\mu_{\epsilon} = (1+\epsilon)^{\alpha-1} + (1-1/\alpha)(2+\epsilon)/\epsilon^2$ for any $\epsilon > 0$. Note that $\eta_{\epsilon} = (1+\epsilon)^{\alpha}\mu_{\epsilon}$.

Lemma 12. Consider any $\epsilon > 0$. Given a power-of- $(1+\epsilon)$ job set \mathcal{J} with an optimal (migratory or non-migratory) schedule \mathcal{S}^* that is immediate-start and bm-proceeding for some $b \ge 1$, Algorithm MAKECRR (with $\lambda = \epsilon$) constructs a CRR(ϵ) schedule \mathcal{S} for \mathcal{J} such that $G(\mathcal{S}) \le (b+1)\mu_{\epsilon}G(\mathcal{S}^*)$, and max-speed(\mathcal{S}) $\le (1+\epsilon) \times max$ -speed(\mathcal{S}^*). ⁵

The rest of this section is devoted to proving Lemma 12. In Section 4.1, we analyze the energy usage. The analysis of flow time is based on an upper bound on the execution time S spends on jobs of certain classes within a period of time. This upper bound is stated and proved in Section 4.2. With this upper bound, we can analyze the flow time in Section 4.3.

Before going into the details of Lemma 12, we show how to exploit Lemma 12 to construct a CRR schedule from any (unrestricted) job set and optimal schedule with the flow time and energy as stated in Theorem 5 (which was first mentioned in Section 2).

Theorem 5. Given a job set \mathcal{J} , let \mathcal{O}_1 and \mathcal{O}_2 be respectively an optimal non-migratory schedule and an optimal migratory schedule for \mathcal{J} using maximum speed T. Then, for any $\epsilon > 0$,

- *i.* we can construct from \mathcal{O}_1 a $CRR(\epsilon)$ schedule S_1 for \mathcal{J} such that $G(S_1) \leq 2\eta_{\epsilon}G(\mathcal{O}_1)$, and $max-speed(S_1) \leq (1+\epsilon)^2 \times max-speed(\mathcal{O}_1)$; and
- ii. we can construct from \mathcal{O}_2 a $CRR(\epsilon)$ schedule \mathcal{S}_2 for \mathcal{J} such that $G(\mathcal{S}_2) \leq 5\eta_{\epsilon}G(\mathcal{O}_2)$, and max-speed $(\mathcal{S}_2) \leq (1+\epsilon)^2 \times max$ -speed (\mathcal{O}_2) .

Proof. We prove the non-migratory case only. The migratory case can be proven in the same way.

First of all, we apply Corollary 10 on \mathcal{J} and \mathcal{O}_1 , and we obtain a power-of- $(1 + \epsilon)$ job set \mathcal{J}' and an immediate-start, optimal non-migratory schedule \mathcal{O}' for \mathcal{J}' with maximum speed $(1 + \epsilon) \times max$ -speed(\mathcal{O}_1). Recall that every optimal non-migratory schedule including \mathcal{O}' is *m*-proceeding.

Next, we apply Algorithm MAKECRR to \mathcal{J}' and \mathcal{O}' and construct a $\operatorname{CRR}(\epsilon)$ schedule \mathcal{S}' for \mathcal{J}' . By Lemma 12, $G_{\mathcal{J}'}(\mathcal{S}') \leq 2\mu_{\epsilon}G_{\mathcal{J}'}(\mathcal{O}')$, and $max\operatorname{-speed}(\mathcal{S}') \leq (1+\epsilon) \times max\operatorname{-speed}(\mathcal{O}')$. By Corollary 10, \mathcal{S}' also defines a $\operatorname{CRR}(\epsilon)$ schedule \mathcal{S}_1 for \mathcal{J} such that $G_{\mathcal{J}}(\mathcal{S}_1) \leq 2(1+\epsilon)^{\alpha}\mu_{\epsilon}G_{\mathcal{J}}(\mathcal{O}_1)$. Note that $max\operatorname{-speed}(\mathcal{S}_1) \leq (1+\epsilon) \times max\operatorname{-speed}(\mathcal{O}') \leq (1+\epsilon)^2 \times max\operatorname{-speed}(\mathcal{O}_1)$.

4.1 Speed and energy

We now start to prove Lemma 12, in which the given job set \mathcal{J} consists of power-of- $(1+\epsilon)$ jobs, \mathcal{S}^* is an optimal schedule that is immediate-start and *bm*-proceeding, and \mathcal{S} is the schedule constructed by Algorithm MAKECRR. We first note that in \mathcal{S} , the speed of a job is $(1+\epsilon)$ times the average speed of *m* jobs in \mathcal{S}^* , so *max-speed*(\mathcal{S}) $\leq (1+\epsilon) \times max-speed$ (\mathcal{S}^*). Next, we consider the energy.

Lemma 13. The energy used by S produced by Algorithm MAKECRR is at most $(b+1)(1 + \epsilon)^{\alpha-1}G(S^*)$.

⁵In general, if Algorithm MAKECRR uses an arbitrary λ , then we have $G(\mathcal{S}) \leq (b+1)((1+\lambda)^{\alpha-1} + (1-1/\alpha)(2+\epsilon)/\lambda\epsilon)G(\mathcal{S}^*)$, and max-speed(\mathcal{S}) $\leq (1+\lambda) \times max$ -speed(\mathcal{S}^*).

Proof. We first note that the energy incurred by running a job of size p at constant speed s is $s^{\alpha}p/s = s^{\alpha-1}p$, which is a convex function of the speed. Consider m jobs of size p being run at different constant speeds, and let x be their average speed. Running a job of size p at speed x incurs energy at most 1/m times the total energy for running these m jobs. If we further increase the speed to $(1 + \epsilon)x$, the power increases by a factor of $(1 + \epsilon)^{\alpha}$, and the running time decreases by a factor of $(1 + \epsilon)$. Thus, the energy usage increases by a factor of $(1 + \epsilon)^{\alpha-1}$. In S, running a job at $(1 + \epsilon)$ times the average speed of m jobs in S^* requires no more energy than $(1 + \epsilon)^{\alpha-1}/m$ times the sum of the energy usage of those m jobs in S^* .

To bound E(S), we use a simple charging scheme: for a job j in S, we charge to every one of the m jobs j' chosen for determining the speed of j in Algorithm MAKECRR; the amount to be charged is 1/m times of the energy usage of j' in S^* . By Algorithm MAKECRR, each job can be charged by at most (b+1)m jobs. Thus,

$$E(\mathcal{S}) \leq \frac{(1+\epsilon)^{\alpha-1}}{m} (b+1)mE(\mathcal{S}^*)$$

$$\leq (b+1)(1+\epsilon)^{\alpha-1}E(\mathcal{S}^*)$$

$$\leq (b+1)(1+\epsilon)^{\alpha-1}G(\mathcal{S}^*) \quad \Box$$

4.2 Upper bound on job execution time of S

To analyze the flow time of a job in S, we attempt to upper bound the execution time of other jobs dispatched to the same processor during its lifespan. Lemmas 14 and 15 below look technical, yet the key observation is quite simple—For any processor z, if we consider all jobs that S dispatches to z during an interval I, excluding the last (b + 1) jobs of each class (size), their total execution time is at most $\ell/(1 + \epsilon)$, where ℓ is the length of I.

Consider any job $h_0 \in \mathcal{J}$. Let h_1, h_2, \ldots, h_n be all the jobs in \mathcal{J} such that $r(h_0) \leq r(h_1) \leq \cdots \leq r(h_n)$ and they have the same size as h_0 . Suppose that $n \geq im$ for some $i \geq b+1$. We focus on two sets of jobs: $\{h_0, h_1, \ldots, h_{im-1}\}$ and $\{h_0, h_m, h_{2m}, \ldots, h_{(i-b-1)m}\}$. The latter contains jobs dispatched to the same processor as h_0 . Lemma 14 below gives an upper bound on the execution time of \mathcal{S} for $\{h_0, h_m, h_{2m}, \ldots, h_{(i-b-1)m}\}$ with respect to \mathcal{S}^* . Roughly speaking, this lemma stems from the fact that \mathcal{S}^* is immediate-start and bm-proceeding as well as Algorithm MAKECRR sets the speed of \mathcal{S} as $(1 + \epsilon)$ times the average speed of certain jobs in \mathcal{S}^* .

Lemma 14. For any job h_0 and $i \ge b+1$, suppose h_{im} exists. Let t be the execution time of S^* for the jobs $h_0, h_1, \ldots, h_{im-1}$ during the interval $[r(h_0), r(h_{im})]$. Then in the entire schedule of S, the total execution time of the jobs $h_0, h_m, \ldots, h_{(i-b-1)m}$ is at most $t/m(1+\epsilon)$.

Proof. Since S^* is immediate-start, jobs h_0, \ldots, h_{im-1} each starts within the interval $[r(h_0), r(h_{im})]$. As S^* is *bm*-proceeding, at time $r(h_{im})$, at most *bm* jobs among these *im* jobs have not yet finished, or equivalently, S^* has completed at least (i - b)m jobs. Let Δ denote a set of any (i - b)m such completed jobs. Based on release times, we partition Δ accordingly into i - b subsets $\Delta_0, \Delta_1, \ldots, \Delta_{i-b-1}$, each of size exactly m. Δ_0 contains the m jobs with smallest release times in Δ , Δ_1 contains jobs with the next m smallest release times in Δ , etc.

Since Δ misses out only bm jobs in $\{h_0, h_1, \ldots, h_{im-1}\}$, each Δ_u , for $u \in \{0, \ldots, i-b-1\}$, is a subset of the (b+1)m jobs $\{h_{um}, h_{um+1}, \ldots, h_{um+(b+1)m-1}\}$. Because the speed used by \mathcal{S} for h_{um} is $(1+\epsilon)$ times the average speed of the m fastest jobs in $h_{um}, h_{um+1}, \ldots, h_{um+(b+1)m-1}$ used by \mathcal{S}^* ,

which is faster than $(1 + \epsilon)$ times the average speed of Δ_u in \mathcal{S}^* , it follows that the execution time of h_{um} in \mathcal{S} is at most $1/m(1 + \epsilon)$ times the total execution time of Δ_u in \mathcal{S}^* . Summing over all $u \in \{0, \ldots, i - b - 1\}$, the execution time of \mathcal{S} for $h_0, h_m, \ldots, h_{(i-b-1)m}$ is no more than $1/m(1 + \epsilon)$ times the total execution time of Δ in \mathcal{S}^* . In \mathcal{S}^* , Δ is only executed during $[r(h_0), r(h_{im})]$, and the lemma follows.

Below is the main result of this section (to be used for analyzing the flow time of S in Section 4.3). Basically, we have proved in Lemma 14 that for any processor z, we can bound the execution time of all jobs that S dispatches to z during an interval I, excluding the last (b + 1) jobs of each class (size). The remaining jobs can be bounded by the fact that the speed used by S^* is at least the critical speed $1/(\alpha - 1)^{1/\alpha}$ (see Section 1.1).

Lemma 15. Consider any k and any time interval I of length ℓ . For jobs of size at most $(1 + \epsilon)^k$ that are released during I, the total execution time of any processor in S for these jobs is at most $\ell/(1 + \epsilon) + (b + 1)(1 + \epsilon)^{k+1} \cdot (\alpha - 1)^{1/\alpha}/\epsilon(1 + \epsilon)$.

Proof. Consider a particular $k' \leq k$. Let $y_{k'}$ be the total execution time over all processors that \mathcal{S}^* uses for jobs of size $(1 + \epsilon)^{k'}$ during the interval I. Consider a particular processor z in \mathcal{S} ; suppose that \mathcal{S} dispatches i jobs of size $(1 + \epsilon)^{k'}$ to processor z during I, and denote these i jobs as $\mathcal{J}' = \{h'_0, h'_m, \ldots, h'_{(i-1)m}\}$, arranged in the order of their release times. We claim that the execution time of processor z in \mathcal{S} for these i jobs is at most $y_{k'}/m(1 + \epsilon)$ plus the execution time of \mathcal{S} for the last b + 1 jobs of \mathcal{J}' . This is obvious if \mathcal{J}' contains b + 1 or fewer jobs. It remains to consider the case when \mathcal{J}' has $i \geq b + 2$ jobs. By Lemma 14, if t is the execution time of \mathcal{S}^* for $h'_0, h'_1, \ldots, h'_{(i-1)m-1}$ during $[r(h'_0), r(h'_{(i-1)m})]$, then \mathcal{S} uses no more than $t/m(1 + \epsilon)$ time to execute $h'_0, h'_m, \ldots, h'_{(i-b-2)m}$. The claim then follows by noticing that $t \leq y_{k'}$, and we only have b + 1 jobs $h'_{(i-b-1)m} \cdots h'_{(i-1)m}$ not being counted.

Now we sum over all $k' \leq k$ the upper bound of these flow times, i.e., $y_{k'}/m(1+\epsilon)$ plus the execution time of \mathcal{S} for the last b+1 jobs in \mathcal{J}' . The sum of the first part is $\sum_{k'\leq k} y_{k'}/m(1+\epsilon)$. Note that $\sum_{k'\leq k} y_{k'}$ is the execution time of \mathcal{S}^* during I, so $\sum_{k'\leq k} y_{k'} \leq m|I| = m\ell$, and the sum of the first part is

$$\frac{\sum_{k' \le k} y_{k'}}{m(1+\epsilon)} \le \frac{\ell}{1+\epsilon}$$

The sum of the second part is over at most b+1 jobs for each k'. Recall that the speed used by \mathcal{S}^* is at least the critical speed $1/(\alpha-1)^{1/\alpha}$ (see Section 1.1), and the speed used by \mathcal{S} is $(1+\epsilon)$ times the average of some job speeds in \mathcal{S}^* . Thus the speed used by \mathcal{S} for any job is at least $(1+\epsilon)/(\alpha-1)^{1/\alpha}$, and the execution time of each job of size $(1+\epsilon)^{k'}$ is at most $(1+\epsilon)^{k'}(\alpha-1)^{1/\alpha}/(1+\epsilon)$. Summing over all k' the execution time for these jobs, we have

$$\sum_{k'=0}^{k} \frac{(b+1)(1+\epsilon)^{k'}(\alpha-1)^{1/\alpha}}{1+\epsilon} < \frac{(b+1)(1+\epsilon)^{k+1}(\alpha-1)^{1/\alpha}}{\epsilon(1+\epsilon)}$$

The lemma follows by summing the two parts.

4.3 Flow time

In this section, we show that the flow time of each job in S is $O(1/\epsilon^2)$ times of its job size (Lemma 16), which implies that the total flow time is $O(1/\epsilon^2)G(S^*)$ (Corollary 17). Together

with Lemma 13, Lemma 12 can be proved. We first bound the flow time of a job of a particular job size in S, making use of Lemma 15.

Lemma 16. In S, the flow time of a job of size $(1+\epsilon)^k$ is at most $(b+1)(2+\epsilon)(1+\epsilon)^k(\alpha-1)^{1/\alpha}/\epsilon^2$.

Proof. Consider a job j of size $(1 + \epsilon)^k$ that is scheduled on some processor z in S. Let r = r(j), and f be the flow time of j in S, i.e., j completes at time r + f. To determine f, we focus on the scheduling of processor z in the intermediate schedule S_0 immediate after Algorithm MAKECRR has scheduled j. Note that f is due to jobs that have been executed in S during [r, r + f]. They can be partitioned into two subsets: \mathcal{J}_1 for jobs released at or before r, and \mathcal{J}_2 for jobs released during (r, r + f]. Let f_1 and f_2 be the contribution on f by \mathcal{J}_1 and \mathcal{J}_2 , respectively, i.e., $f = f_1 + f_2$.

We first consider \mathcal{J}_1 . Let t be the last time before r such that processor z is idle right before t in \mathcal{S}_0 . Thus all jobs executed by processor z at or after t, and hence all jobs in \mathcal{J}_1 , must be released at or after t. By Lemma 15, the execution time of processor z for jobs in \mathcal{J}_1 is no more than $(r-t)/(1+\epsilon) + [(b+1)(1+\epsilon)^{k+1}(\alpha-1)^{1/\alpha}/\epsilon(1+\epsilon)]$. Since processor z is busy throughout [t,r), the amount of execution time for jobs in \mathcal{J}_1 remaining at r is at most

$$\frac{r-t}{1+\epsilon} + \frac{(b+1)(1+\epsilon)^{k+1}(\alpha-1)^{1/\alpha}}{\epsilon(1+\epsilon)} - (r-t) \\ \leq \frac{(b+1)(1+\epsilon)^{k+1}(\alpha-1)^{1/\alpha}}{\epsilon(1+\epsilon)} .$$

This implies $f_1 \le (b+1)(1+\epsilon)^{k+1}(\alpha-1)^{1/\alpha}/\epsilon(1+\epsilon)$.

Next we consider \mathcal{J}_2 . Since Algorithm MAKECRR schedules jobs from the smallest to the largest size, jobs in \mathcal{J}_2 are of size at most $(1 + \epsilon)^{k-1}$. We apply Lemma 15 to the interval [r, r + f] for jobs of size at most $(1 + \epsilon)^{k-1}$. The execution time of processor z for jobs in \mathcal{J}_2 , i.e., f_2 , is no more than

$$\frac{f}{1+\epsilon} + \frac{(b+1)(1+\epsilon)^k(\alpha-1)^{1/\alpha}}{\epsilon(1+\epsilon)}$$

Then we have

$$f = f_1 + f_2 \le \frac{(b+1)(2+\epsilon)(1+\epsilon)^k(\alpha-1)^{1/\alpha}}{\epsilon(1+\epsilon)} + \frac{f}{1+\epsilon}$$

immediately implying $f \leq (b+1)(2+\epsilon)(1+\epsilon)^k(\alpha-1)^{1/\alpha}/\epsilon^2$.

Summing over all jobs and recalling that $G(\mathcal{S}^*) \geq \frac{\alpha}{(\alpha-1)^{1-1/\alpha}} p(\mathcal{J})$ (see Lemma 1), we have the following corollary.

Corollary 17. The total flow time incurred by S produced by Algorithm MAKECRR is at most $((b+1)(1-1/\alpha)(2+\epsilon)/\epsilon^2)G(S^*)$.

By Lemma 13 and Corollary 17, we have $G(\mathcal{S}) = E(\mathcal{S}) + F(\mathcal{S}) \leq (b+1)[(1+\epsilon)^{\alpha-1} + (1-1/\alpha)(2+\epsilon)/\epsilon^2]G(\mathcal{S}^*) = (b+1)\mu_{\epsilon}G(\mathcal{S}^*)$. We have also noted that $max-speed(\mathcal{S}) \leq (1+\epsilon) \times max-speed(\mathcal{S}^*)$. Hence, Lemma 12 follows.

5 Optimal migratory schedules

Algorithm MAKECRR and Lemma 12 can be applied to an optimal migratory schedule as long as it is immediate-start and *bm*-proceeding for some integer $b \ge 1$. Note that the *m*-proceeding property or even the 4*m*-proceeding property does not hold for every optimal migratory schedule. Nevertheless, Lemma 18 below shows that at least one optimal schedule satisfies the 4*m*-proceeding property (i.e., at any time, there are at most 4*m* jobs of the same size started but not yet completed). Once we know the existence of such schedule, we can apply the construction in Algorithm MAKEISO and Lemma 9 to further modify the job set and the schedule to obtain an optimal migratory schedule that is 4*m*-proceeding and immediate-start (since the only manipulation done by Algorithm MAKEISO is to swap the schedule of pairs of jobs). The rest of the arguments then follow, leading to Theorem 5 (ii).

Lemma 18. For any job set \mathcal{J} , there exists an optimal migratory schedule \mathcal{S}^* that is 4*m*-proceeding.

The rest of this section is devoted to proving the above lemma. Recall that *m*-proceeding property holds for every optimal non-migratory schedule. For optimal migratory schedules, we find that some of them, which we call *lazy-start* optimal migratory schedules, satisfy the 4*m*-proceeding property. The definition is as follows: Given a schedule S, we define its "start time sequence" to be the sequence of start time of each job, sorted in the increasing order of time. Among all optimal migratory schedules (which may or may not be immediate-start), a lazy-start optimal schedule is the one with lexicographically maximum start time sequence. Such a schedule has the following property.

Lemma 19. In a lazy-start optimal schedule, suppose a job j_1 starts at time t, while another job j_2 of the same size that has already started before t but has not finished at t is not running at t. Then after t, j_1 runs whenever j_2 runs.

Proof. Suppose the contrary, and let t' be the first time after t that j_2 runs but j_1 does not. Let p_0 be the amount of work processed for j_1 during [t, t'] when j_2 is not running. We divide the analysis into three cases, each arriving at a contradiction.

Case 1: j_1 is not yet completed by t'. We can exchange some x > 0 units of work of j_2 starting from t' with the x units of work of j_1 starting from t, without changing processor speed at any time. The start time of j_1 is thus delayed without changing the start times of other jobs or increasing the energy or flow time, so the original schedule is not lazy-start.

Case 2: j_1 is completed by t', and the amount of work processed for j_2 after t' is at most p_0 . We can exchange all work of j_2 after t' with some work of j_1 starting from t. The completion times of j_1 and j_2 are exchanged, but the total flow time and energy is preserved. The start time of j_1 is delayed without changing the start times of other jobs, so the original schedule is not lazy-start.

Case 3: j_1 is completed by t', and the amount of work processed for j_2 after t' is more than p_0 . Since j_1 and j_2 are of the same size, these conditions imply that there must be some work processed for j_1 when both j_1 and j_2 are running. Furthermore, j_2 must be running slower than j_1 during this period, otherwise the total amount of work processed for j_2 would be larger than the size of j_1 , so the two jobs cannot be of the same size. Since jobs run at constant speed in optimal schedules, the speed of j_1 is higher than the speed of j_2 .

Note that j_1 lags behind j_2 at t but is ahead of j_2 at t'. So there must be a time $t_0 \in (t', t)$ such that j_1 and j_2 has been processed for the same amount of work. Exchange the scheduling of j_1

and j_2 after t_0 gives a schedule with the completion time of j_1 and j_2 exchanged, while the energy consumption and flow time remain the same. But now j_1 and j_2 are not running at constant speed, so the schedule is not optimal.

Now we are ready to prove Lemma 18 by showing that a lazy-start optimal migratory schedule is 4m-proceeding.

Proof of Lemma 18. Given a job set \mathcal{J} , let \mathcal{S} be a lazy-start optimal migratory schedule. Suppose, for the sake of contradiction, that at some time in \mathcal{S} , there are 4m jobs started but not yet finished. Consider these 4m jobs. At the time when the (m + i)-th job j starts, at least i jobs which started earlier must be idle. For each such idling job j', Lemma 19 dictates that after r(j), whenever j' runs, j must also be running. In this case, we say that j' implies j. Since there are 4m jobs, there are $1 + 2 + \cdots + 3m = \frac{3}{2}m(3m + 1)$ such relations. Thus some job j_0 implies at least $\frac{3}{2}m(3m + 1)/4m > m$ other jobs. After all these other jobs are released, they must all run whenever j_0 runs. This contradicts that there are only m processors. The lemma follows.

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